



Docket No.: GR 98 P 2499 P

Gr P 2814
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By: *[Signature]*

Date: January 6, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#12 IDS
J. Brunson
1/29/03

Applicant : Thomas Peter Haneder et al.
Applic. No. : 09/801,209
Filed : March 7, 2001
Title : Ferroelectric Transistor, Use Thereof In A Memory Cell Configuration And Method Of Producing The Ferroelectric Transistor
Examiner : Thao X. Le - Art Unit: 2814

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

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In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,731,608 (Hsu et al.), dated March 24, 1998;

United States Patent No. 5,932,904 (Hsu et al.), dated August 3, 1999.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,

Mark P. Weichselbaum
Reg. No. 43,248



For Applicants

Date: January 6, 2003

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